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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cheng-Wen Wu, et. al) Examiner: Britt, C.
Serial No.: 10/735,298) Art Unit: 2138
Filed: December 12, 2003) Our Ref: B-5320 621551-4
For: "Multi-Port Memory Testing Method...") Date: April 10, 2006
	Re: Response to Office Action

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is filed in response to the Official Action dated January 24, 2006.

Claim Amendments begin on Page 2 thereof. Applicant's Remarks, which are made without prejudice, begin on Page 7 of this response.